# Effects of High Temperature Annealing on Tungsten-doped VO, Phase Transition Temperature Abigail Rothert, Materials Science and Engineering Mentor: Liping Wang, Associate Professor SEMTE

## **Research Question**

This project seeks to address how annealing tungsten-doped vanadium dioxide at high temperatures affects the phase transition temperature and the change in transmittance between the metallic and insulating phases.

## **Motivation**

The insulator to metal phase transition of undoped VO<sub>2</sub> happens around 68°C, and if the phase transition temperature could be lowered to ambient temperature, this material would be even more useful as a thermal control for spacecraft or smart window applications.

#### Results

The phase transition temperature for the VO<sub>2</sub> sample annealed at 700°C for 1 hour happens from 55-90°C, as seen in figure a. The hysteresis of this sample was larger than those annealed at lower temperatures, while it was expected that at higher annealing temperatures the hysteresis would be smaller.



a) Heating cooling curve of VO<sub>2</sub> annealed at 700°C for 1 hr where the phase transition occurs around 75°C. b) Heating cooling curve of 1% W-doped VO<sub>2</sub> annealed at 750°C for 1 hr where the phase transition occurs around 46°C.



#### Methods

Samples of 25 nm tungsten-doped vanadium on silicon substrates are annealed in a furnace and during this process the vanadium is oxidized and becomes VO<sub>2</sub>. FTIR is used to measure the transmittance of the sample as it is being heated and cooled 5°C at a time. As seen in figures a and b, the change in transmittance measured reveals the phase transition temperature.

#### **Future Work**

By lowering the oxygen concentration in the furnace during the annealing process, the amount of  $V_2O_5$  on the surface could be reduced and lead to more accurate measurements.



